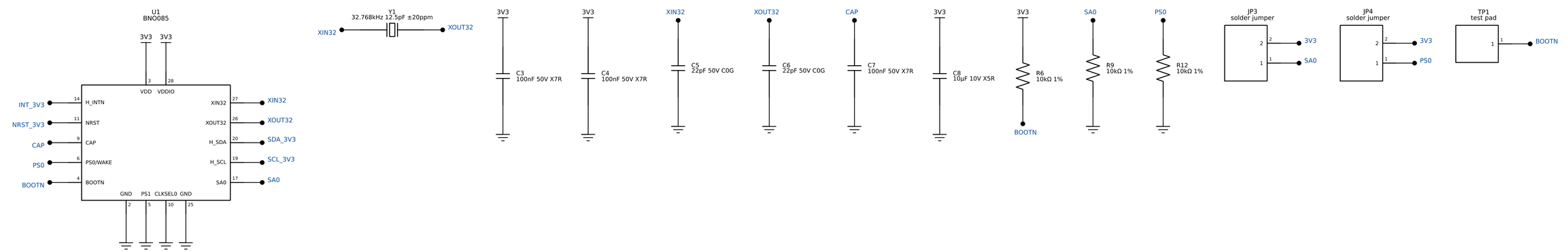
MAIN CIRCUIT AND SUPPORT



LEVEL SHIFTING AND PULL-UPS



POWER AND INDICATORS

CONNECTORS

NOTES

Net-label schematic: pins with the same blue net name are connected.

Generated from the verified netlist in the design file rev B (BOM ↔ netlist check passed).

Values and LCSC part numbers: see section 8 of the design file.

No-connect: U1.1, 7, 8, 12, 13, 15 ENV_SDA, 16 ENV_SCL, 18 H_CSN, 21–24 → leave unconnected

JP1 (default Closed): Cut to turn off the LED

JP2 (default Closed): Cut to remove the host-side I2C pull-ups

JP3 (default Open): Bridge → SA0=3V3 → address 0x4B

JP4 (default Open): Bridge → PS0=3V3 → UART-RVC mode (simple 100Hz heading/pitch/roll stream)

ELECTROTRICAL

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ET-S04 BNO085 9-DoF Orientation IMU (sensor-fusion) Breakout

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Drawn from netlist · JLCPCB parts · Do not scale