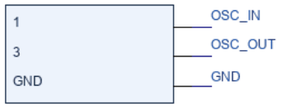
Main Circuit And Support

Y1
8MHz crystal 20pF ±10ppm

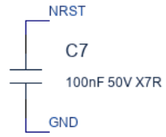


SW1
Tactile switch 5.1x5.1mm SMD

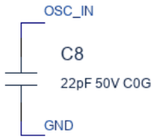


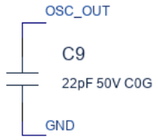
SW2
Tactile switch 5.1x5.1mm SMD

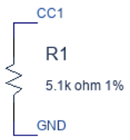


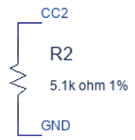


Main Circuit And Support



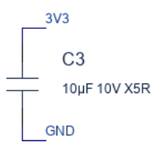


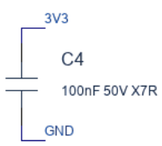


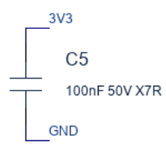


Main Circuit And Support

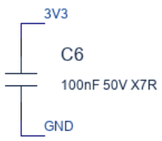


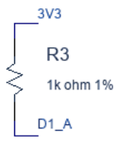


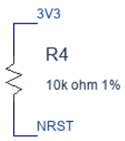


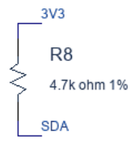


Main Circuit And Support

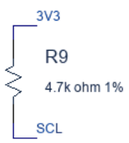








Main Circuit And Support



DESIGN NOTES

Matching net labels identify connected signals.

Assembly and DNP details are stored with each component.

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PROJECT / DOCUMENT			STATUS
Ch32V203 Usbc Riscv Board			DESIGN REVIEW
DRAWN BY	CHECKED BY	DATE	REVISION
ElectroTrical	-	2026-11-12	A
DOCUMENT NUMBER		FORMAT	SHEET
ET-M16-SCH-001		A3 / LANDSCAPE	1 of 3

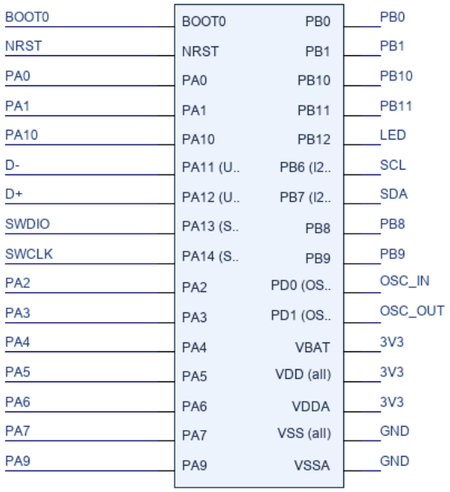
ET-M16

Power And Indicators

Power And Indicators

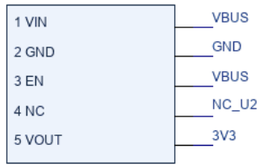
U1

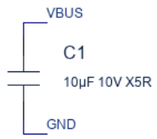
CH32V203C8T6 RISC-V 144MHz, 64KB flash, USB FS

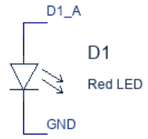


U2

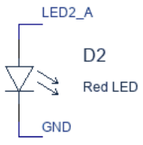
AP2112K-3.3 LDO 600mA

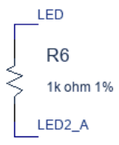


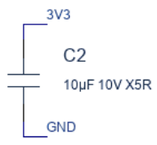




Power And Indicators







DESIGN NOTES

Matching net labels identify connected signals.

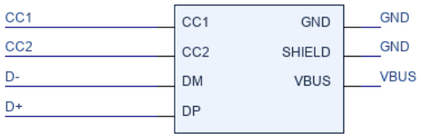
Assembly and DNP details are stored with each component.

ELECTROTRICAL.COM

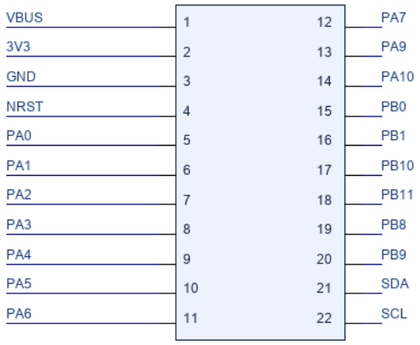
PROJECT / DOCUMENT			STATUS
Ch32V203 Usbc Riscv Board			DESIGN REVIEW
DRAWN BY	CHECKED BY	DATE	REVISION
ElectroTrical	-	2026-11-12	A
DOCUMENT NUMBER		FORMAT	SHEET
ET-M16-SCH-002		A3 / LANDSCAPE	2 of 3

Connectors

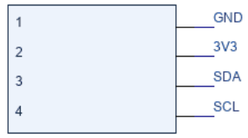
J1
USB-C 16P receptacle (USB 2.0)



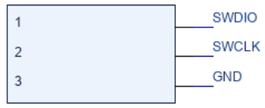
J3 DNP
2.54mm male pin header (ship loose)



J4
JST SH 4P R/A (Qwiic/STEMMA QT)



J5 DNP
2.54mm male pin header (ship loose)



DESIGN NOTES

Matching net labels identify connected signals.

Assembly and DNP details are stored with each component.

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PROJECT / DOCUMENT			STATUS
Ch32V203 Usbc Riscv Board			DESIGN REVIEW
DRAWN BY	CHECKED BY	DATE	REVISION
ElectroTrical	-	2026-11-12	A
DOCUMENT NUMBER		FORMAT	SHEET
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