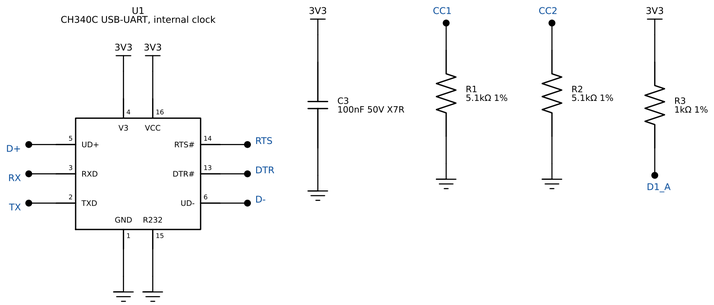
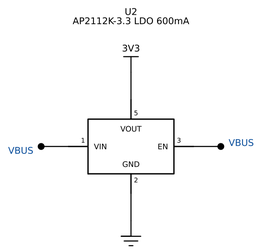
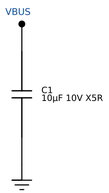
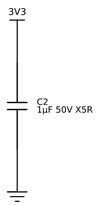
MAIN CIRCUIT AND SUPPORT

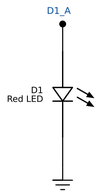


POWER AND INDICATORS

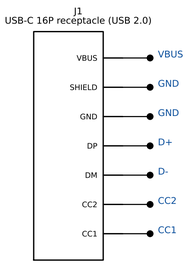


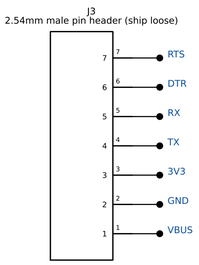






CONNECTORS





NOTES

Net-label schematic: pins with the same blue net name are connected.

Generated from the verified netlist in the design file rev A (BOM ↔ netlist check passed).

Values and LCSC part numbers: see section 8 of the design file.

No-connect: U2.4 NC; U1.7 NC; U1.8 NC; U1.9 CTS#; U1.10 DSR#; U1.11 RI#; U1.12 DCD#

ELECTROTRICAL

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ET-C03 USB-C to UART Adapter (CH340C, 3.3V logic)

Rev A · 11 Nov 2026 · Sheet 1/1

Drawn from netlist · JLCPCB parts · Do not scale