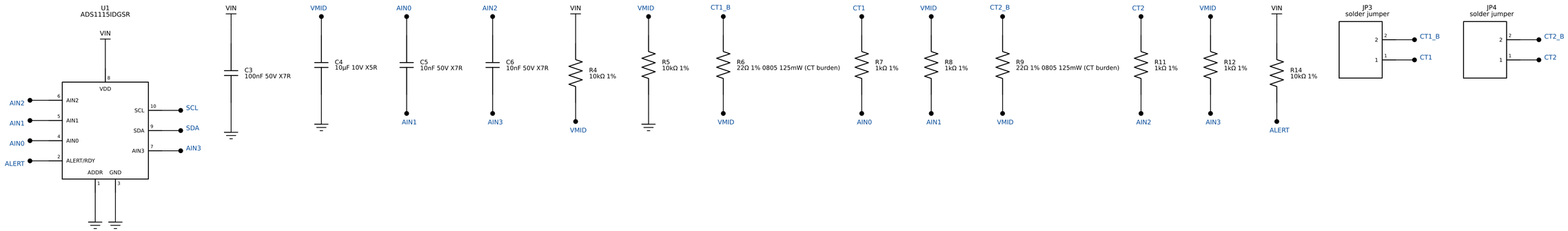
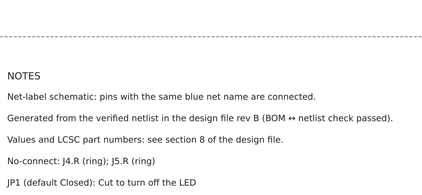
MAIN CIRCUIT AND SUPPORT



LEVEL SHIFTING AND PULL-UPS



POWER AND INDICATORS

CONNECTORS

NOTES

Net-label schematic: pins with the same blue net name are connected.

Generated from the verified netlist in the design file rev B (BOM ↔ netlist check passed).

Values and LCSC part numbers: see section 8 of the design file.

No-connect: J4.R (ring); J5.R (ring)

JP1 (default Closed): Cut to turn off the LED

JP2 (default Closed): Cut to remove the host-side I2C pull-ups

JP3 (default Closed): CT1 burden. Cut for voltage-output CTs (SCT-013-030/050 etc.)

JP4 (default Closed): CT2 burden. Cut for voltage-output CTs

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ET-S38 2-Channel CT Clamp Interface (SCT-013) with 16-bit I2C ADC

Rev B · 10 Nov 2026 · Sheet 1/1

Drawn from netlist · JLCPCB parts · Do not scale