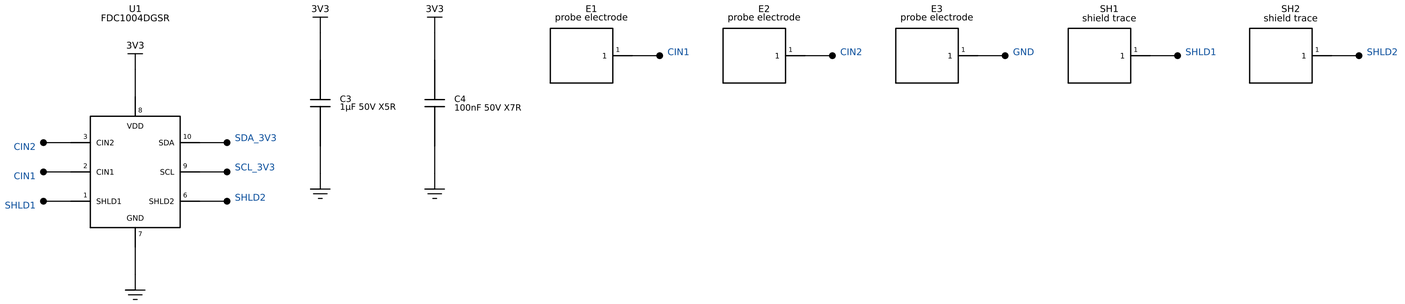
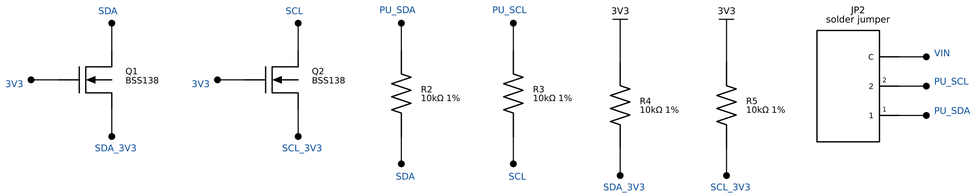
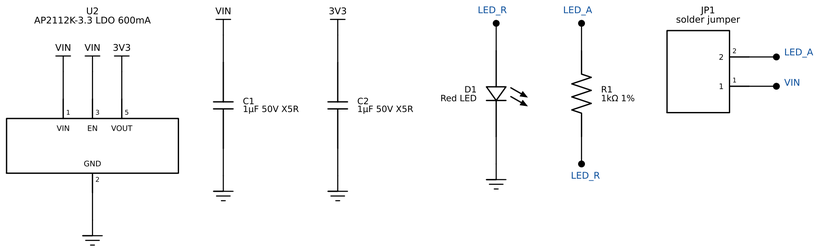
MAIN CIRCUIT AND SUPPORT



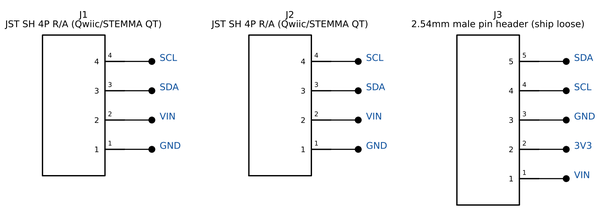
LEVEL SHIFTING AND PULL-UPS



POWER AND INDICATORS



CONNECTORS



NOTES

Net-label schematic: pins with the same blue net name are connected.

Generated from the verified netlist in the design file rev B (BOM ↔ netlist check passed).

Values and LCSC part numbers: see section 8 of the design file.

No-connect: U1.4 CIN3, U1.5 CIN4 → test pads only (leave open per TI)

JP1 (default Closed): Cut to turn off the LED (saves power on battery nodes)

JP2 (default Closed): Cut to remove the host-side pull-ups

ELECTROTRICAL

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ET-S10 Capacitive Soil-Moisture Probe (FDC1004, I2C, corrosion-proof)

Rev B · 10 Nov 2026 · Sheet 1/1

Drawn from netlist · JLCPCB parts · Do not scale