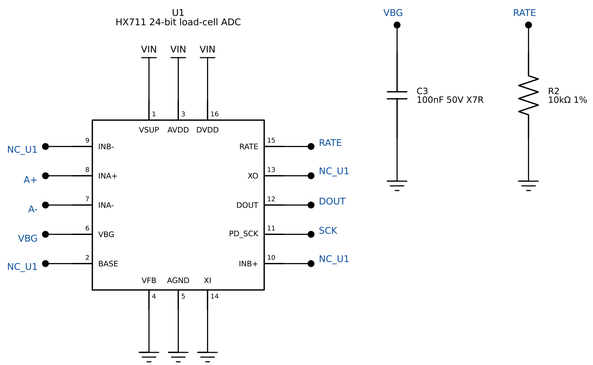
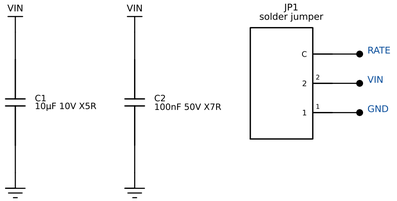
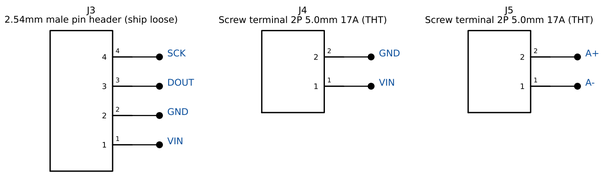
MAIN CIRCUIT AND SUPPORT



POWER AND INDICATORS



CONNECTORS



NOTES

Net-label schematic: pins with the same blue net name are connected.

Generated from the verified netlist in the design file rev A (BOM ↔ netlist check passed).

Values and LCSC part numbers: see section 8 of the design file.

JP1 (default 10Hz): 3-way: 1-C = GND (10Hz, 50/60Hz rejection), 2-C = VIN (80Hz)

ELECTROTRICAL

ElectroTrical LLC · Dubai

ET-S67 HX711 Load Cell Amplifier (24-bit, 10/80Hz, screw terminals)

Rev A · 11 Nov 2026 · Sheet 1/1

Drawn from netlist · JLCPCB parts · Do not scale