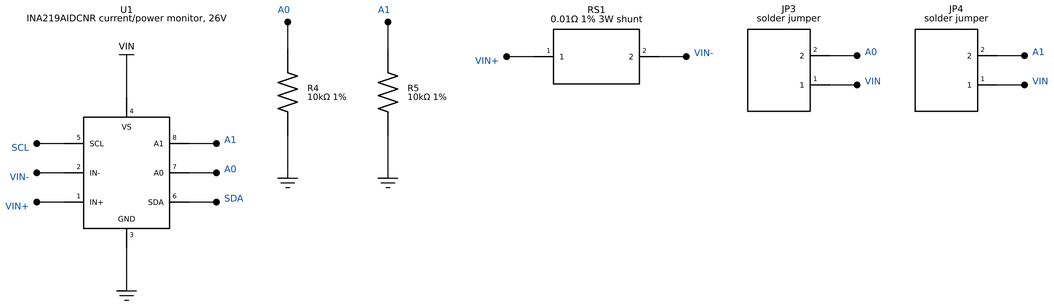
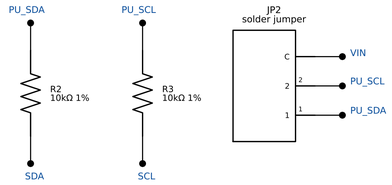
MAIN CIRCUIT AND SUPPORT

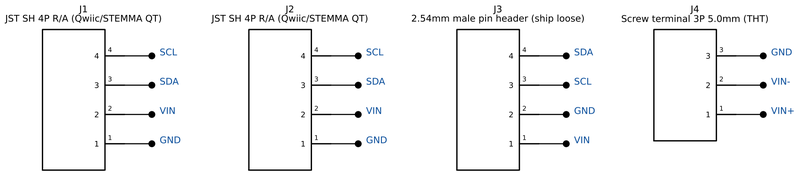


LEVEL SHIFTING AND PULL-UPS



POWER AND INDICATORS

CONNECTORS



NOTES

Net-label schematic: pins with the same blue net name are connected.

Generated from the verified netlist in the design file rev A (BOM ↔ netlist check passed).

Values and LCSC part numbers: see section 8 of the design file.

JP1 (default Closed): Cut to turn off the LED

JP2 (default Closed): Cut to remove the host-side I2C pull-ups

JP3, JP4 (default Open): Bridge A0 / A1 to VIN → 0x41, 0x44, 0x45

ELECTROTRICAL

ElectroTrical LLC · Dubai

ET-S52 INA219 High-Current Monitor (26V, 10A, 0.01Ω shunt, Qwiic)

Rev A · 11 Nov 2026 · Sheet 1/1

Drawn from netlist · JLCPCB parts · Do not scale