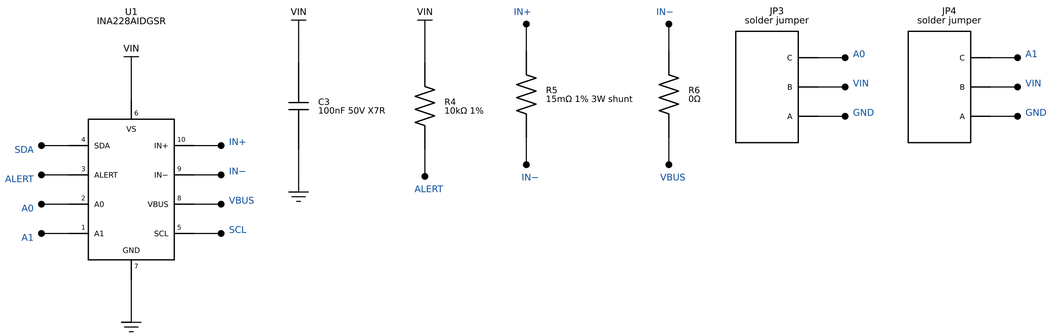
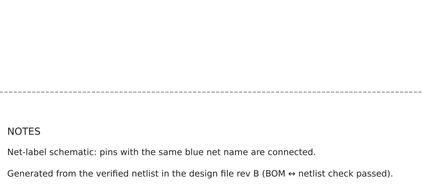
## MAIN CIRCUIT AND SUPPORT



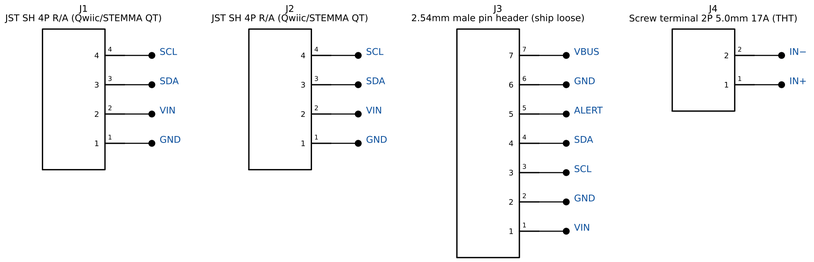

## LEVEL SHIFTING AND PULL-UPS




## POWER AND INDICATORS




## CONNECTORS




NOTES

Net-label schematic: pins with the same blue net name are connected.

Generated from the verified netlist in the design file rev B (BOM ↔ netlist check passed).

Values and LCSC part numbers: see section 8 of the design file.

JP1 (default Closed): Cut to turn off the LED

JP2 (default Closed): Cut to remove the I2C pull-ups

JP3 (default A–C (GND)): A0: cut and bridge B–C → VS

JP4 (default A–C (GND)): A1: cut and bridge B–C → VS

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Drawn from netlist · JLCPCB parts · Do not scale