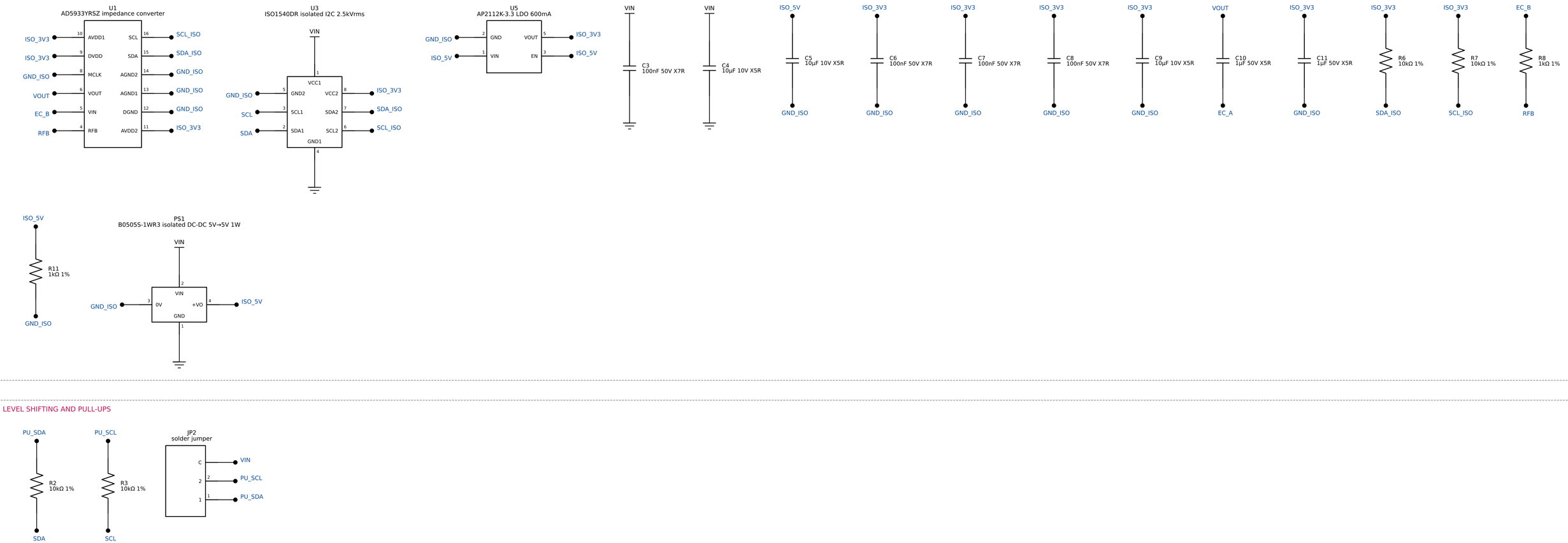
MAIN CIRCUIT AND SUPPORT



LEVEL SHIFTING AND PULL-UPS

POWER AND INDICATORS

CONNECTORS

NOTES

Net-label schematic: pins with the same blue net name are connected.

Generated from the verified netlist in the design file rev B (BOM ↔ netlist check passed).

Values and LCSC part numbers: see section 8 of the design file.

No-connect: U1.1 NC; U1.2 NC; U1.3 NC; U1.7 NC

JP1 (default Closed): Cut to turn off the LED

JP2 (default Closed): Cut to remove the host-side I2C pull-ups

ELECTROTRICAL

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ET-S46 Isolated Electrical Conductivity (EC/TDS) Interface – AD5933 Impedance Analyzer, BNC

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Drawn from netlist · JLCPCB parts · Do not scale