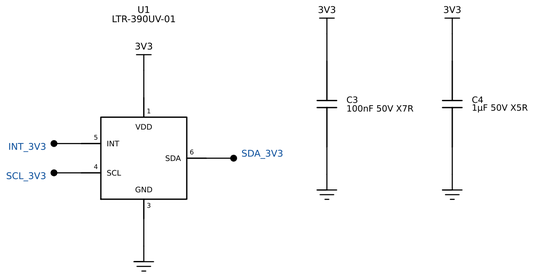
## MAIN CIRCUIT AND SUPPORT

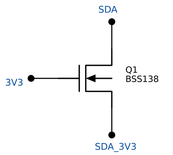


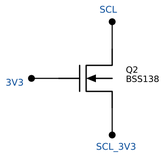


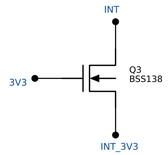


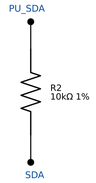


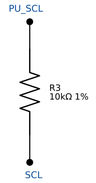

## LEVEL SHIFTING AND PULL-UPS

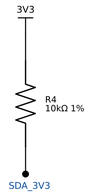


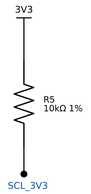



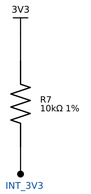



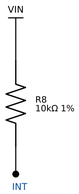



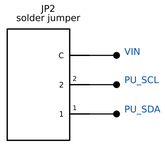



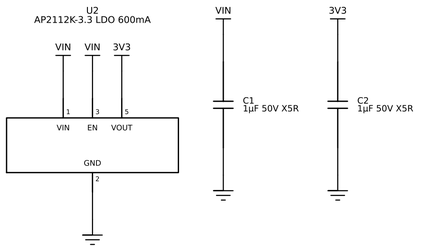




## POWER AND INDICATORS

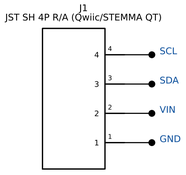


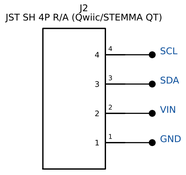


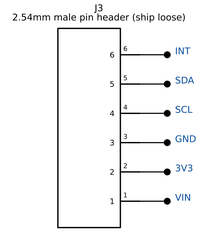





## CONNECTORS




NOTES

Net-label schematic: pins with the same blue net name are connected.

Generated from the verified netlist in the design file rev B (BOM ↔ netlist check passed).

Values and LCSC part numbers: see section 8 of the design file.

No-connect: U1.2 NC → leave open

JP2 (default Closed): Cut to remove the host-side I2C pull-ups

ELECTROTRICAL

ElectroTrical LLC · Dubai

ET-S17 LTR-390UV UV Index + Ambient Light Breakout

Rev B · 10 Nov 2026 · Sheet 1/1

Drawn from netlist · JLCPCB parts · Do not scale