ELECTROTRICAL

HARDWARE ENGINEERING / SCHEMATIC

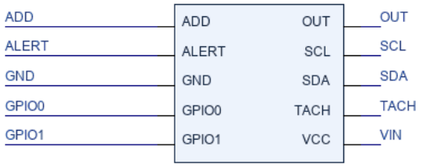
ET-MD18

Main Circuit And Support / Level Shifting And Pull-Ups

Main Circuit And Support

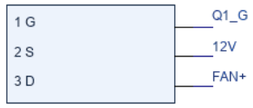
U1

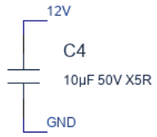
MAX6650EUB+ I2C fan speed regulator with tach feedback

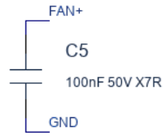


Q1

AO3401A P-MOSFET 30V 4A

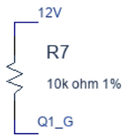


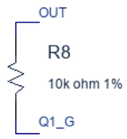




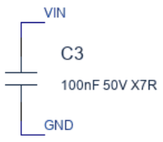
Main Circuit And Support

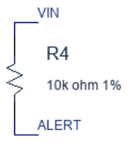










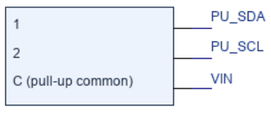


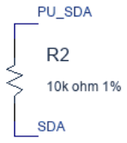


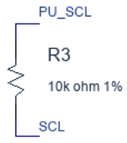
Level Shifting And Pull-Ups

JP2

Solder jumper







DESIGN NOTES

Matching net labels identify connected signals.

Assembly and DNP details are stored with each component.

ELECTROTRICAL.COM

PROJECT / DOCUMENT			STATUS
Max6650 I2C Fan Speed Controller			DESIGN REVIEW
DRAWN BY	CHECKED BY	DATE	REVISION
ElectroTrical	-	2026-11-12	A
DOCUMENT NUMBER		FORMAT	SHEET
ET-MD18-SCH-001		A3 / LANDSCAPE	1 of 2

ET-MD18

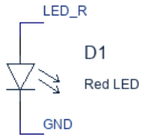
Power And Indicators / Connectors

Power And Indicators

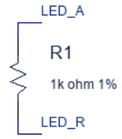
JP1

Solder jumper

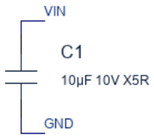








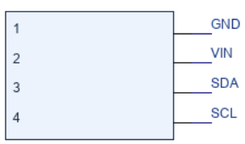
Power And Indicators



Connectors

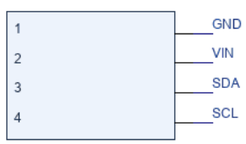
J1

JST SH 4P R/A (Qwiic/STEMMA QT)



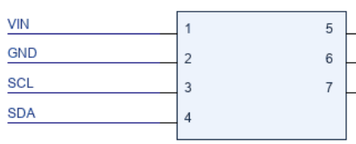
J2

JST SH 4P R/A (Qwiic/STEMMA QT)



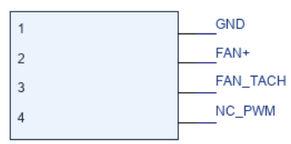
J3 DNP

2.54mm male pin header (ship loose)



J4 DNP

2.54mm male pin header (ship loose)



Connectors

J5

Screw terminal 2P 5.0mm 17A (THT)



DESIGN NOTES

Matching net labels identify connected signals.

Assembly and DNP details are stored with each component.

ELECTROTRICAL.COM

PROJECT / DOCUMENT			STATUS
Max6650 I2C Fan Speed Controller			DESIGN REVIEW
DRAWN BY	CHECKED BY	DATE	REVISION
ElectroTrical	-	2026-11-12	A
DOCUMENT NUMBER		FORMAT	SHEET
ET-MD18-SCH-002		A3 / LANDSCAPE	2 of 2