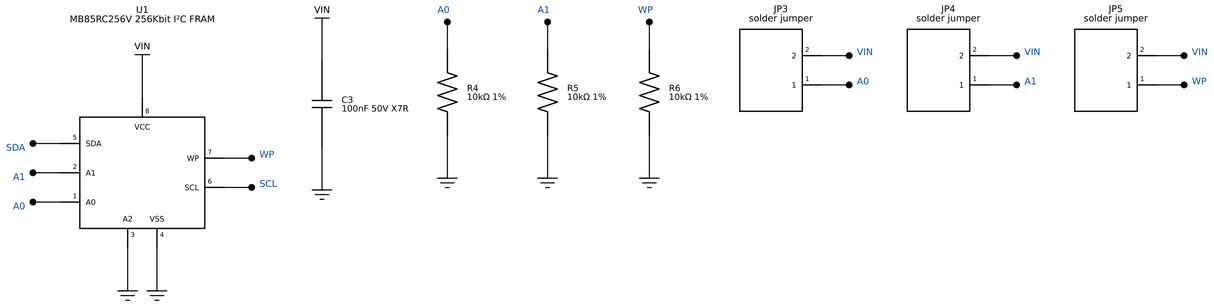
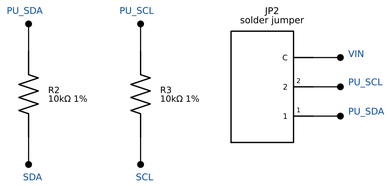
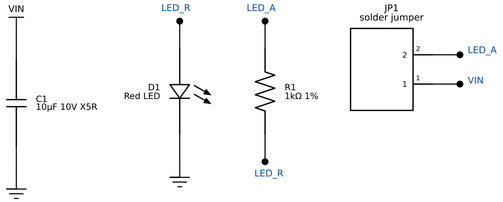
MAIN CIRCUIT AND SUPPORT



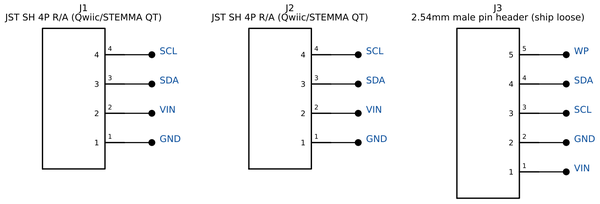
LEVEL SHIFTING AND PULL-UPS



POWER AND INDICATORS



CONNECTORS



NOTES

Net-label schematic: pins with the same blue net name are connected.

Generated from the verified netlist in the design file rev A (BOM ↔ netlist check passed).

Values and LCSC part numbers: see section 8 of the design file.

JP1 (default Closed): Cut to turn off the LED

JP2 (default Closed): Cut to remove the host-side I2C pull-ups

JP3 (default Open): Bridge → A0 high

JP4 (default Open): Bridge → A1 high

JP5 (default Open): Bridge → WP high (lock)

ELECTROTRICAL

ElectroTrical LLC · Dubai

ET-A14 MB85RC256V I²C FRAM (32KB, 10^{12} writes, no write delay, Qwiic)

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Drawn from netlist · JLCPCB parts · Do not scale