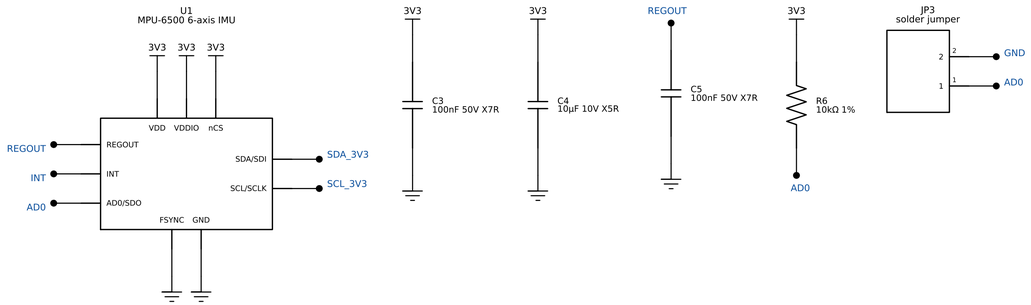
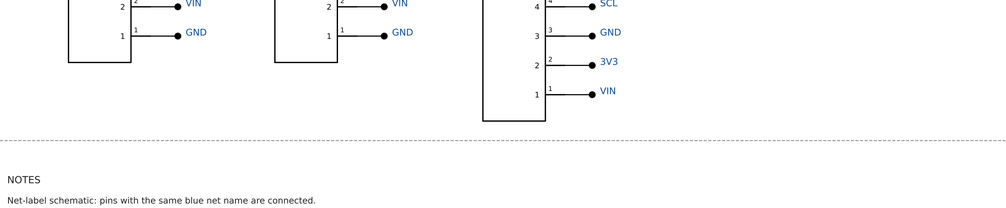
MAIN CIRCUIT AND SUPPORT



LEVEL SHIFTING AND PULL-UPS



POWER AND INDICATORS

CONNECTORS

NOTES

Net-label schematic: pins with the same blue net name are connected.

Generated from the verified netlist in the design file rev A (BOM ↔ netlist check passed).

Values and LCSC part numbers: see section 8 of the design file.

JP1 (default Closed): Cut to turn off the LED

JP2 (default Closed): Cut to remove the host-side I2C pull-ups

JP3 (default Open): Bridge → AD0 low → 0x68

ELECTROTRICAL

ElectroTrical LLC · Dubai

ET-S63 MPU-6500 6-DoF IMU (accel + gyro, MPU-6050-compatible registers, Qwiic)

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Drawn from netlist · JLCPCB parts · Do not scale