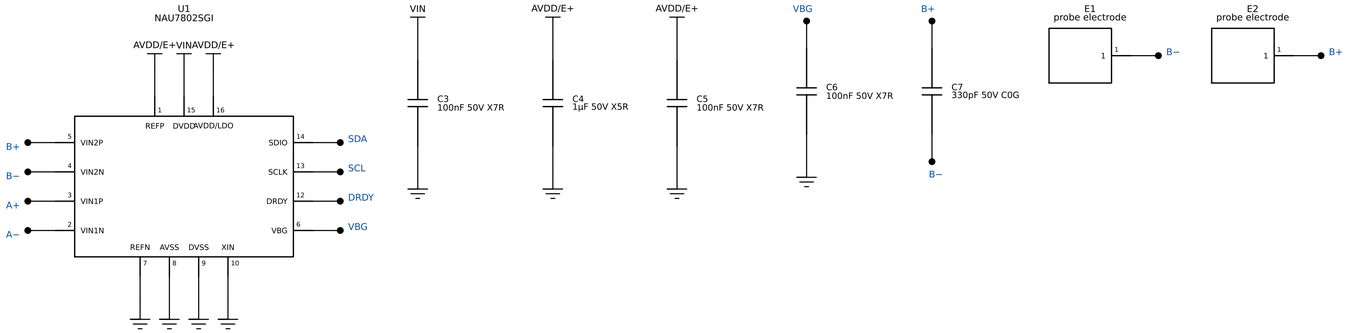
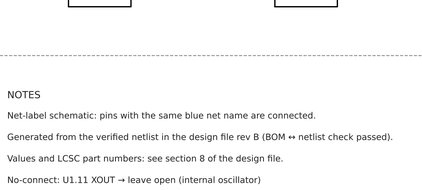
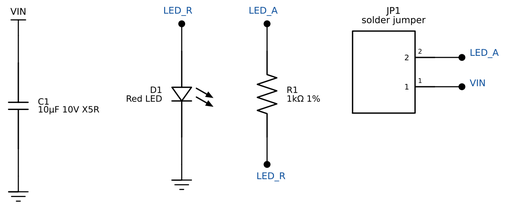
MAIN CIRCUIT AND SUPPORT



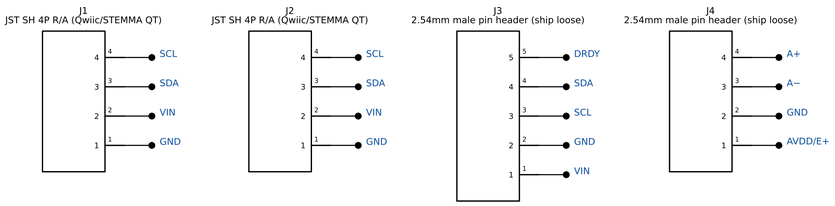
LEVEL SHIFTING AND PULL-UPS



POWER AND INDICATORS



CONNECTORS



NOTES

Net-label schematic: pins with the same blue net name are connected.

Generated from the verified netlist in the design file rev B (BOM ↔ netlist check passed).

Values and LCSC part numbers: see section 8 of the design file.

No-connect: U1.11 XOUT → leave open (internal oscillator)

JP1 (default Closed): Cut to turn off the LED

JP2 (default Closed): Cut to remove the I2C pull-ups

ELECTROTRICAL

ElectroTrical LLC · Dubai

ET-S09 NAU7802 24-bit Load-Cell / Bridge ADC (Qwiic Scale)

Rev B · 10 Nov 2026 · Sheet 1/1

Drawn from netlist · JLCPCB parts · Do not scale