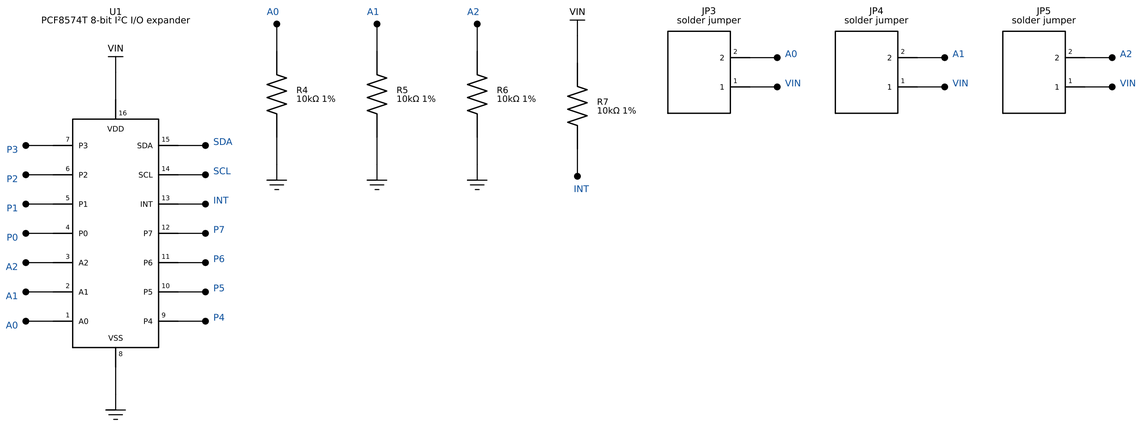
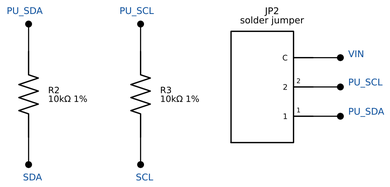
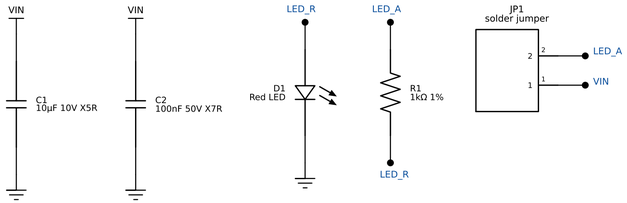
## MAIN CIRCUIT AND SUPPORT



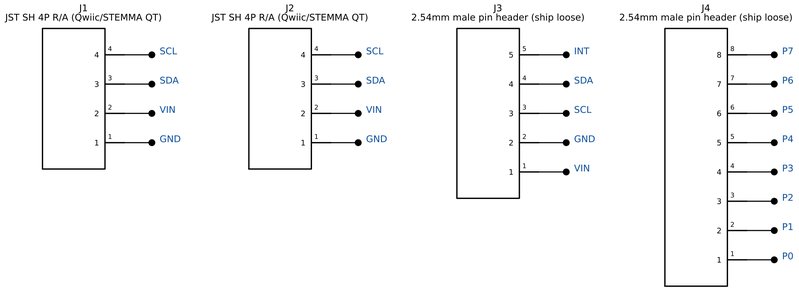

## LEVEL SHIFTING AND PULL-UPS




## POWER AND INDICATORS




## CONNECTORS




NOTES

Net-label schematic: pins with the same blue net name are connected.

Generated from the verified netlist in the design file rev A (BOM ↔ netlist check passed).

Values and LCSC part numbers: see section 8 of the design file.

JP1 (default Closed): Power LED (cut = off)

JP2 (default Closed): I²C pull-ups (cut = off)

JP3–JP5 (default Open): Bridge to set A0–A2 → address 0x20–0x27

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ET-A07 PCF8574 8-Bit I/O Expander (I²C, Qwiic)

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Drawn from netlist · JLCPCB parts · Do not scale