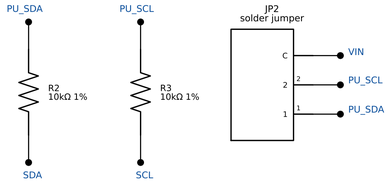
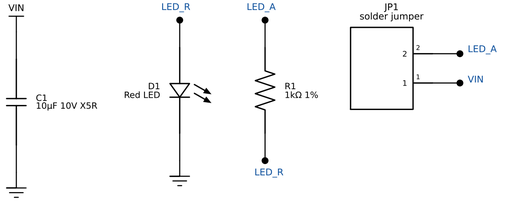
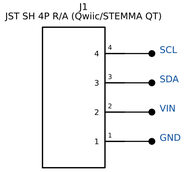
LEVEL SHIFTING AND PULL-UPS

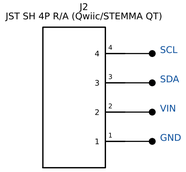


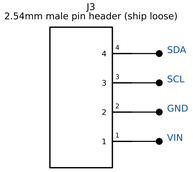
POWER AND INDICATORS



CONNECTORS







NOTES

Net-label schematic: pins with the same blue net name are connected.

Generated from the verified netlist in the design file rev A (BOM ↔ netlist check passed).

Values and LCSC part numbers: see section 8 of the design file.

JP1 (default Closed): Power LED (cut = off)

JP2 (default Closed): I²C pull-ups (cut = off)

ELECTROTRICAL

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ET-A10 Qwiic Proto Board (2 ports + 0.1" prototyping grid)

Rev A · 10 Nov 2026 · Sheet 1/1

Drawn from netlist · JLCPCB parts · Do not scale