Main Circuit And Support

U3
W25Q16JVUXIQ 16Mbit QSPI flash

QSPI_SS	1 /CS	5 DI/IO0	QSPI_SD0
QSPI_SD1	2 DO/IO1	6 CLK	QSPI_SCLK
QSPI_SD2	3 /WP/IO2	7 /HOLD..	QSPI_SD3
GND	4 GND	8 VCC	3V3

Y1
12MHz crystal 20pF ±10ppm

1		XIN
3		XOUT_R
GND		GND

RUN — C10 — 100nF 50V X7R — GND

DVDD — C11 — 4.7µF 16V X5R — GND

Main Circuit And Support

DVDD — C12 — 4.7µF 16V X5R — GND

XIN — C13 — 15pF 50V C0G — GND

XOUT_R — C14 — 15pF 50V C0G — GND

CC1 — R1 — 5.1k ohm 1% — GND

Main Circuit And Support

3V3 — C3 — 10µF 10V X5R — GND

3V3 — C4 — 100nF 50V X7R — GND

3V3 — C5 — 100nF 50V X7R — GND

3V3 — C6 — 100nF 50V X7R — GND

Main Circuit And Support

3V3 — C7 — 100nF 50V X7R — GND

3V3 — C8 — 100nF 50V X7R — GND

3V3 — C9 — 100nF 50V X7R — GND

3V3 — C15 — 100nF 50V X7R — GND

DESIGN NOTES

Matching net labels identify connected signals.

Assembly and DNP details are stored with each component.

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PROJECT / DOCUMENT: Rp2350 Usbc Devboard			STATUS: DESIGN REVIEW
DRAWN BY: ElectroTrical	CHECKED BY: -	DATE: 2026-11-12	REVISION: A
DOCUMENT NUMBER: ET-M12-SCH-001		FORMAT: A3 / LANDSCAPE	SHEET: 1 of 4

Main Circuit And Support

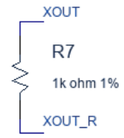
SW1
Tactile switch 5.1x5.1mm SMD



SW2
Tactile switch 5.1x5.1mm SMD



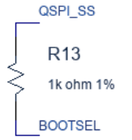




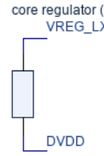
Main Circuit And Support



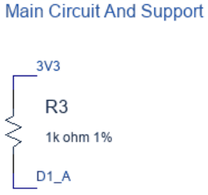




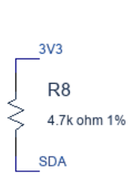
L1
3.3µH polarised inductor for the RP2350 core regulator (Raspberry Pi reference)



Main Circuit And Support

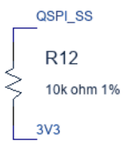








Main Circuit And Support



DESIGN NOTES

Matching net labels identify connected signals.

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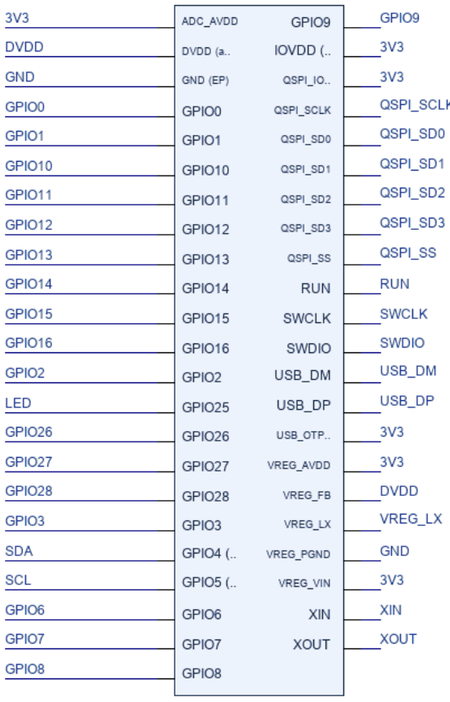
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Rp2350 Usbc Devboard			DESIGN REVIEW
DRAWN BY	CHECKED BY	DATE	REVISION
ElectroTrical	-	2026-11-12	A
DOCUMENT NUMBER		FORMAT	SHEET
ET-M12-SCH-002		A3 / LANDSCAPE	2 of 4

Power And Indicators

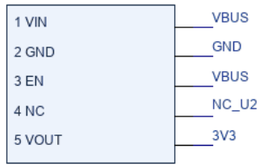
U1

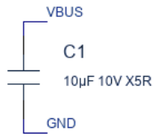
RP2350A dual Cortex-M33 / Hazard3
RISC-V 150MHz, 520KB RAM

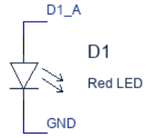


U2

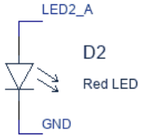
AP2112K-3.3 LDO 600mA

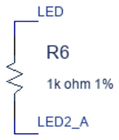


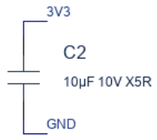




Power And Indicators







DESIGN NOTES

Matching net labels identify connected signals.

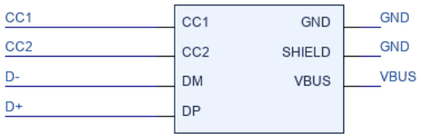
Assembly and DNP details are stored with each component.

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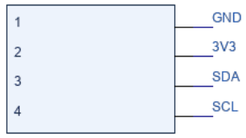
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Rp2350 Usbc Devboard			DESIGN REVIEW
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ElectroTrical	-	2026-11-12	A
DOCUMENT NUMBER		FORMAT	SHEET
ET-M12-SCH-003		A3 / LANDSCAPE	3 of 4

Connectors

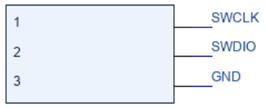
J1
USB-C 16P receptacle (USB 2.0)



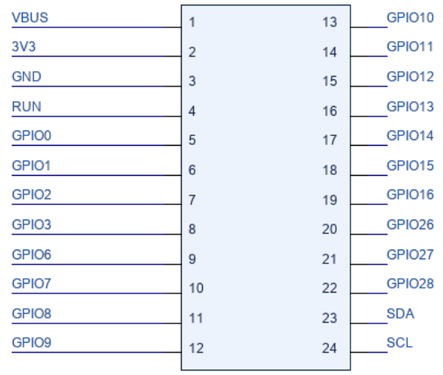
J4
JST SH 4P R/A (Qwiic/STEMMA QT)



J5 DNP
2.54mm male pin header (ship loose)



J3 DNP
2.54mm male pin header (ship loose)



DESIGN NOTES

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ElectroTrical	-	2026-11-12	A
DOCUMENT NUMBER		FORMAT	SHEET
ET-M12-SCH-004		A3 / LANDSCAPE	4 of 4