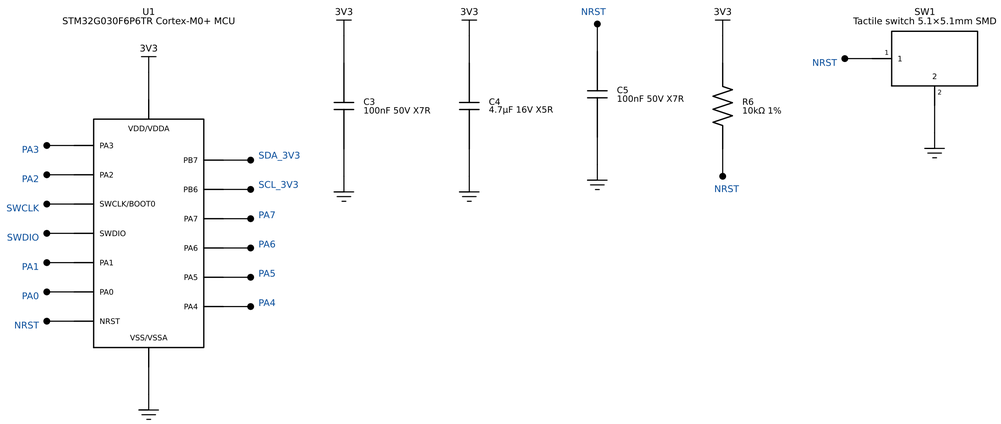
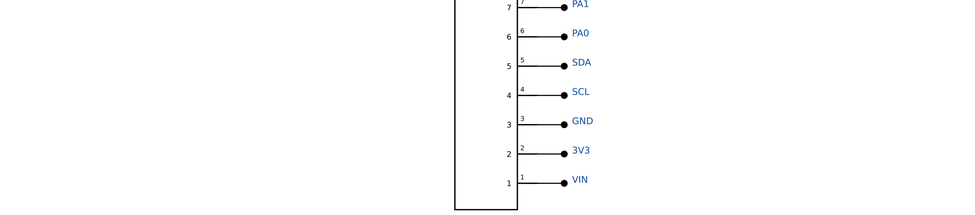
MAIN CIRCUIT AND SUPPORT



LEVEL SHIFTING AND PULL-UPS



POWER AND INDICATORS

CONNECTORS

NOTES

Net-label schematic: pins with the same blue net name are connected.

Generated from the verified netlist in the design file rev A (BOM ↔ netlist check passed).

Values and LCSC part numbers: see section 8 of the design file.

JP1 (default Closed): Cut to turn off the LED

JP2 (default Closed): Cut to remove the host-side I2C pull-ups

ELECTROTRICAL

ElectroTrical LLC · Dubai

ET-M06 STM32G030 Cortex-M0+ Qwiic MCU Board (64MHz, LDO + level-shifted I2C, SWD)

Rev A · 11 Nov 2026 · Sheet 1/1

Drawn from netlist · JLCPCB parts · Do not scale