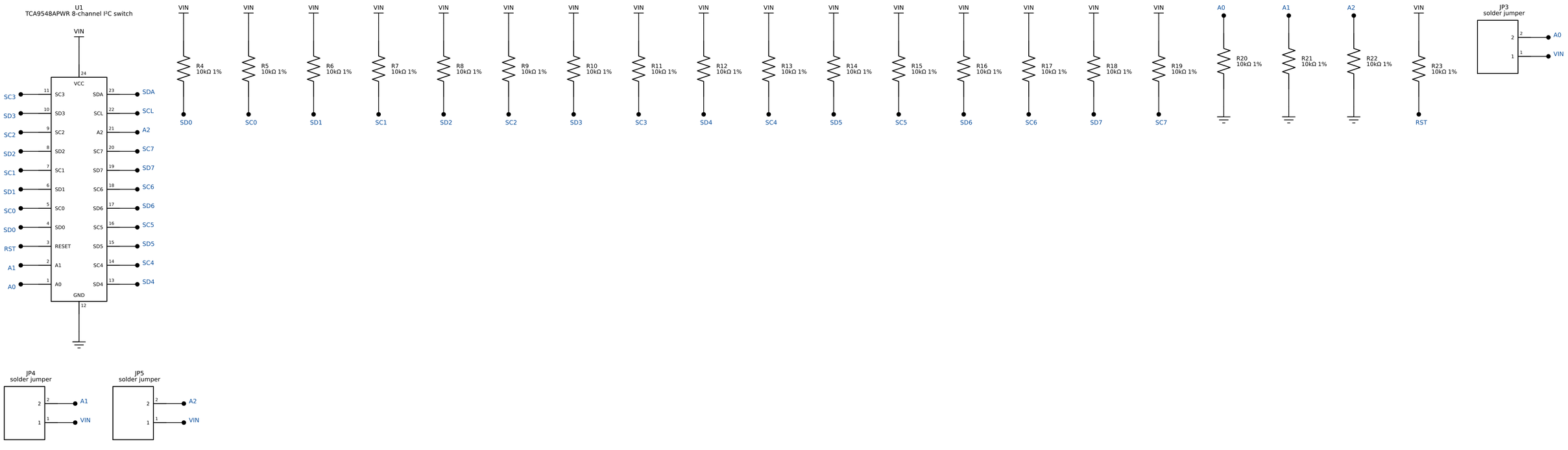
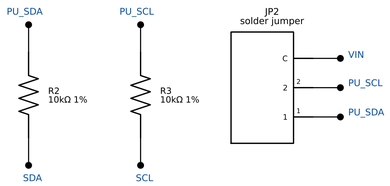
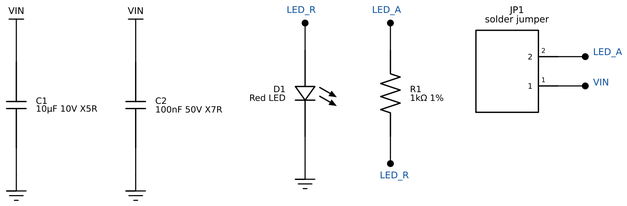
MAIN CIRCUIT AND SUPPORT



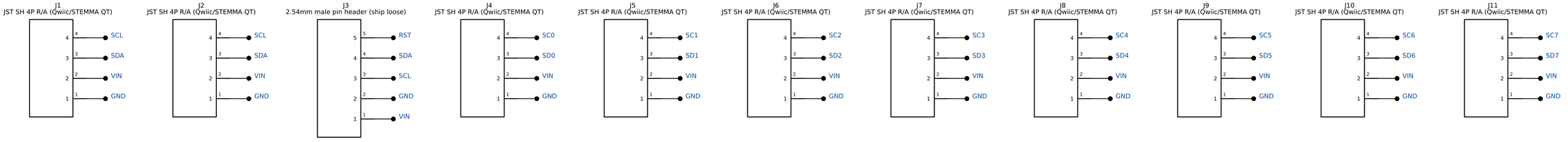
LEVEL SHIFTING AND PULL-UPS



POWER AND INDICATORS



CONNECTORS



NOTES

Net-label schematic: pins with the same blue net name are connected.

Generated from the verified netlist in the design file rev A (BOM ↔ netlist check passed).

Values and LCSC part numbers: see section 8 of the design file.

JP1 (default Closed): Power LED (cut = off)

JP2 (default Closed): I²C pull-ups (cut = off)

JP3–JP5 (default Open): Bridge to set A0–A2 → address 0x70–0x77

ELECTROTRICAL	ElectroTrical LLC · Dubai
ET-A02 TCA9548A 8-Channel I²C Multiplexer (Qwiic on every channel)	
Rev A · 10 Nov 2026 · Sheet 1/1	
Drawn from netlist · JLCPCB parts · Do not scale	