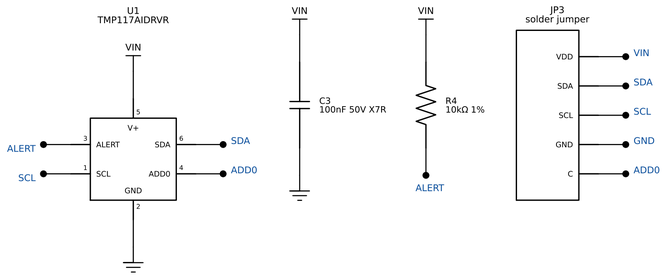
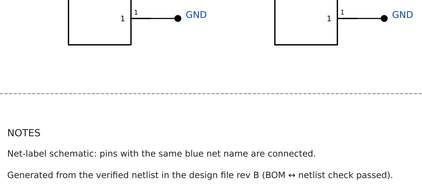
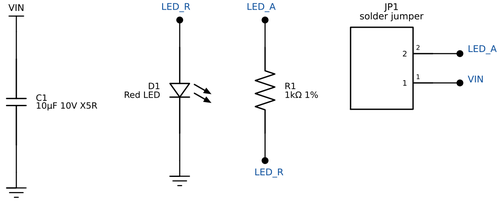
MAIN CIRCUIT AND SUPPORT



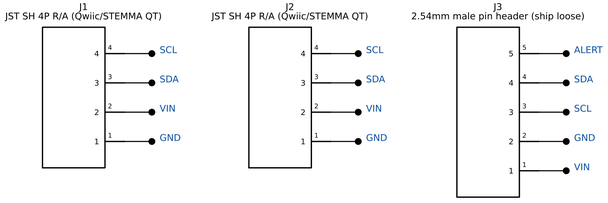
LEVEL SHIFTING AND PULL-UPS



POWER AND INDICATORS



CONNECTORS



NOTES

Net-label schematic: pins with the same blue net name are connected.

Generated from the verified netlist in the design file rev B (BOM ↔ netlist check passed).

Values and LCSC part numbers: see section 8 of the design file.

JP1 (default Closed): Cut to turn off the LED

JP2 (default Closed): Cut to remove the host-side I2C pull-ups

JP3 (default C–GND): 4-way address: GND 0x48 · VDD 0x49 · SDA 0x4A · SCL 0x4B

ELECTROTRICAL

ElectroTrical LLC · Dubai

ET-S16 TMP117 ±0.1°C Precision Digital Temperature Breakout

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Drawn from netlist · JLCPCB parts · Do not scale