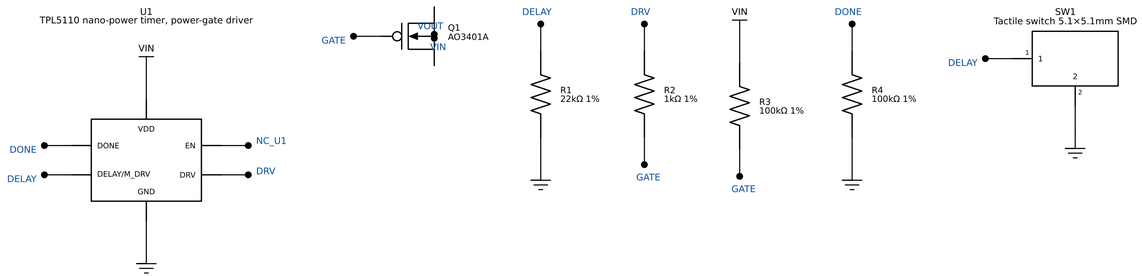
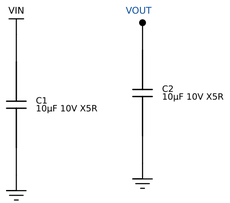
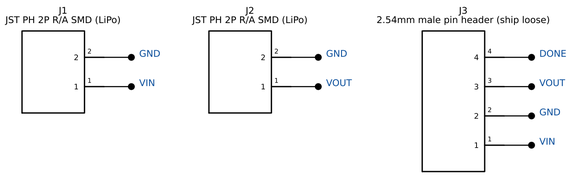
MAIN CIRCUIT AND SUPPORT



POWER AND INDICATORS



CONNECTORS



NOTES

Net-label schematic: pins with the same blue net name are connected.

Generated from the verified netlist in the design file rev A (BOM ↔ netlist check passed).

Values and LCSC part numbers: see section 8 of the design file.

ELECTROTRICAL

ElectroTrical LLC · Dubai

ET-P19 TPL5110 Low-Power Timer Power Switch (35nA, 100ms–2h, P-FET gate)

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Drawn from netlist · JLCPCB parts · Do not scale