ELECTROTRICAL

HARDWARE ENGINEERING / SCHEMATIC

ET-A22

Main Circuit And Support / Power And Indicators

Main Circuit And Support

U1

W25Q128JVSIQ 128Mbit (16MB) SPI NOR flash

CS_3V3	1 /CS	5 DI	DI_3V3
DO_3V3	2 DO	6 CLK	CLK_3V3
WP#	3 /WP	7 /HOLD	HOLD#
GND	4 GND	8 VCC	3V3

U3

TXS0108E 8-bit bidirectional level translator

CS_3V3	A1	B3	MOSI
CLK_3V3	A2	B4	MISO
DI_3V3	A3	GND	GND
DO_3V3	A4	OE	OE
CS	B1	VCCA	3V3
SCK	B2	VCCB	VIN

3V3 — C3 — 100nF 50V X7R — GND

VIN — C5 — 100nF 50V X7R — GND

Main Circuit And Support

3V3 — R1 — 10k ohm 1% — CS_3V3

3V3 — R2 — 10k ohm 1% — WP#

3V3 — R3 — 10k ohm 1% — HOLD#

3V3 — R4 — 10k ohm 1% — OE

Power And Indicators

U2

AP2112K-3.3 LDO 600mA

1 VIN	VIN
2 GND	GND
3 EN	VIN
4 NC	NC_U2
5 VOUT	3V3

VIN — C1 — 10µF 10V X5R — GND

3V3 — C2 — 1µF 50V X5R — GND

DESIGN NOTES

Matching net labels identify connected signals.

Assembly and DNP details are stored with each component.

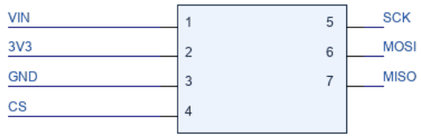
ELECTROTRICAL.COM

PROJECT / DOCUMENT: W25Q128 16Mb Spi Flash			STATUS: DESIGN REVIEW
DRAWN BY: ElectroTrical	CHECKED BY: -	DATE: 2026-11-12	REVISION: A
DOCUMENT NUMBER: ET-A22-SCH-001		FORMAT: A3 / LANDSCAPE	SHEET: 1 of 2

Connectors

J3 DNP

2.54mm male pin header (ship loose)



DESIGN NOTES

Matching net labels identify connected signals.

Assembly and DNP details are stored with each component.

ELECTROTRICAL.COM

PROJECT / DOCUMENT			STATUS
W25Q128 16Mb Spi Flash			DESIGN REVIEW
DRAWN BY	CHECKED BY	DATE	REVISION
ElectroTrical	-	2026-11-12	A
DOCUMENT NUMBER		FORMAT	SHEET
ET-A22-SCH-002		A3 / LANDSCAPE	2 of 2