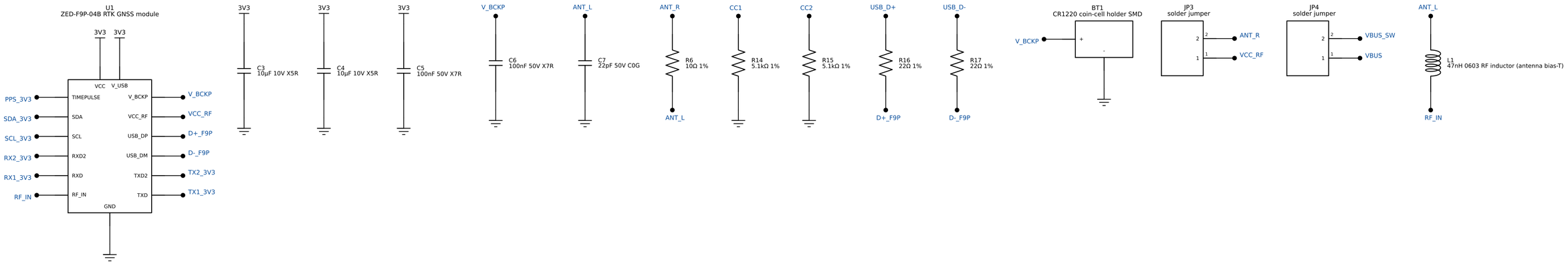
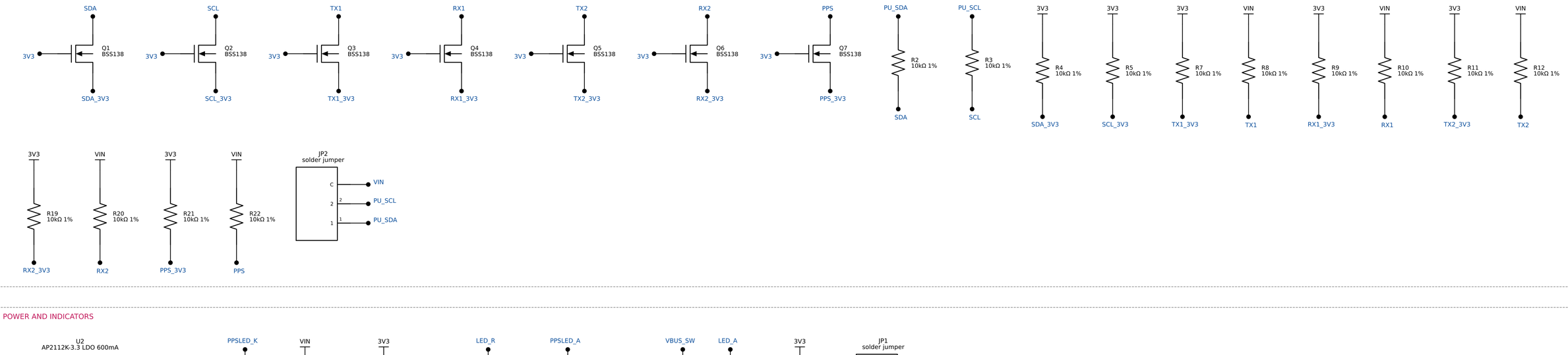
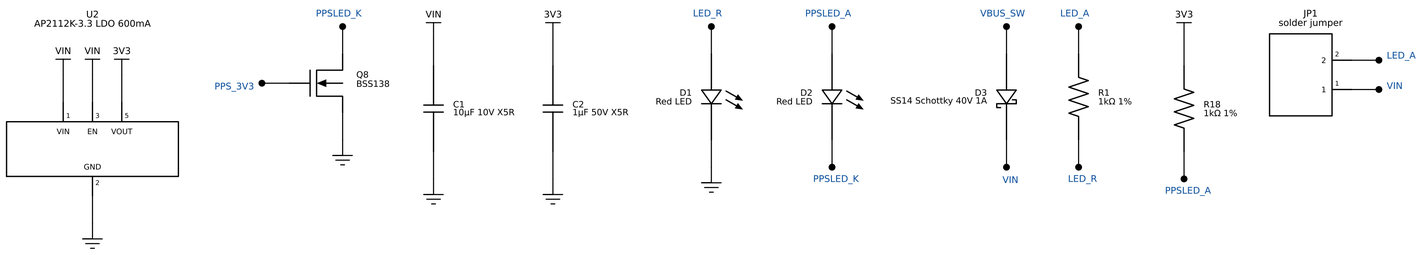
MAIN CIRCUIT AND SUPPORT



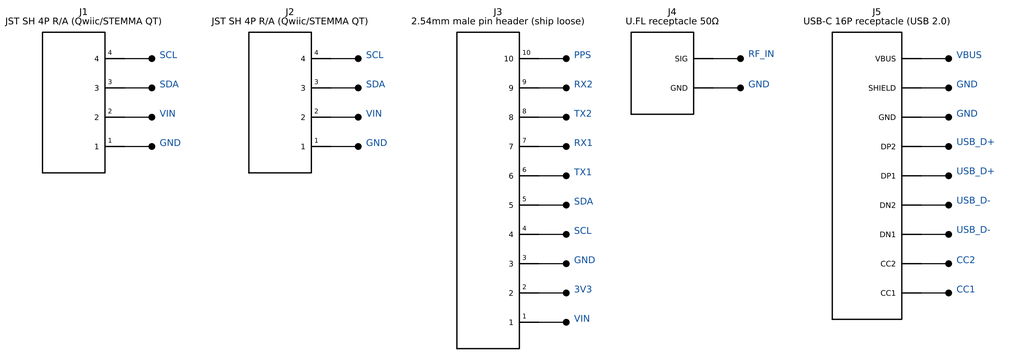
LEVEL SHIFTING AND PULL-UPS



POWER AND INDICATORS



CONNECTORS



NOTES

Net-label schematic: pins with the same blue net name are connected.

Generated from the verified netlist in the design file rev B (BOM ↔ netlist check passed).

Values and LCSC part numbers: see section 8 of the design file.

No-connect: U1.RESET_N / SAFEBOOT_N (test pads TP1/TP2); U1.EXTINT; U1.reserved

JP1 (default Closed): Cut to turn off the LED

JP2 (default Closed): Cut to remove the host-side I2C pull-ups

JP3 (default Closed): Active-antenna bias ON (multi-band antennas are active). Cut for passive/externally powered antennas

JP4 (default Open): Close to power the board from USB-C alone. Keep open when Qwiic/J3 also supplies VIN

ELECTROTRICAL

ElectroTrical LLC · Dubai

ET-S42 ZED-F9P Multi-Band RTK GNSS (cm-level) with USB-C, Qwiic and Dual UART

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Drawn from netlist · JLCPCB parts · Do not scale